



200504US-2

\*RESPONSE UNDER 37 CFR 1.116-  
EXPEDITED PROCEDURES EXAMINER?  
GROUP 2811

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

TAKUJI MATSUMOTO ET AL.

SERIAL NO: 09/729,816

FILED: DECEMBER 6, 2000

FOR: SEMICONDUCTOR DEVICE AND  
METHOD OF MANUFACTURING  
THE SAME

: GROUP ART UNIT: 2811

: EXAMINER: OWENS, D.

#17/B (TUE)  
1/30/03  
Adm th  
Ents  
3/25/03

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

In response to the Official Action mailed October 21, 2002, please amend this  
application as follows:

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IN THE CLAIMS

Please amend Claims 1, 2-4 and 7 to read as follows:<sup>1</sup>

1. (Twice Amended) A semiconductor device comprising:

an SOI substrate including a substrate in which at least its surface is insulative and a  
semiconductor layer provided on said surface of said substrate, said semiconductor layer  
having a first active region of a first conductivity type and a second active region of the first  
conductivity type both of which are provided in a main surface thereof;

an isolation insulating film formed between said first and second active regions in  
said main surface of said semiconductor layer, leaving a first semiconductor region which is

<sup>1</sup>A marked-up copy of the changes made to this claim is attached.

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Sub  
C